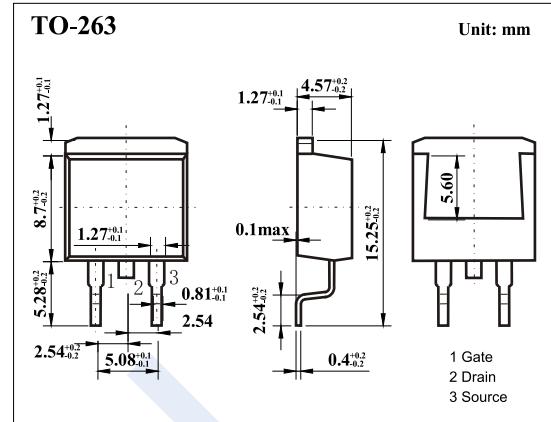
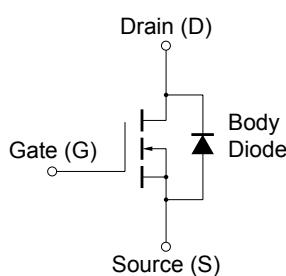


## N-Channel MOSFET

## 2SK3305-ZJ

## ■ Features

- $V_{DS(on)} = 500V$
- $I_D = 5A$  ( $V_{GS} = 10V$ )
- $R_{DS(on)} < 1.5\Omega$  ( $V_{GS} = 10V$ )
- Gate voltage rating:  $\pm 30V$
- Avalanche capability ratings

■ Absolute Maximum Ratings  $T_a = 25^\circ C$ 

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	500	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	
Continuous Drain Current	$I_D$	5	A
Pulsed Drain Current (Note.1)	$I_{DM}$	20	
Single Avalanche Current (Note.2)	$I_{AS}$	5	W
Power Dissipation $T_c = 25^\circ C$ $T_a = 25^\circ C$	$P_D$	75	
		1.5	
Single Avalanche Energy (Note.2)	$E_{AS}$	125	mJ
Junction Temperature	$T_J$	150	$^\circ C$
Storage Temperature Range	$T_{stg}$	-55 to 150	

Note.1: PW  $\leq 10\text{ }\mu\text{s}$ , Duty Cycle  $\leq 1\%$

Note.2: Starting  $T_J = 25^\circ C$ ,  $V_{DD} = 150\text{ V}$ ,  $R_G = 25\Omega$ ,  $V_{GS} = 20\text{ V} \rightarrow 0\text{ V}$

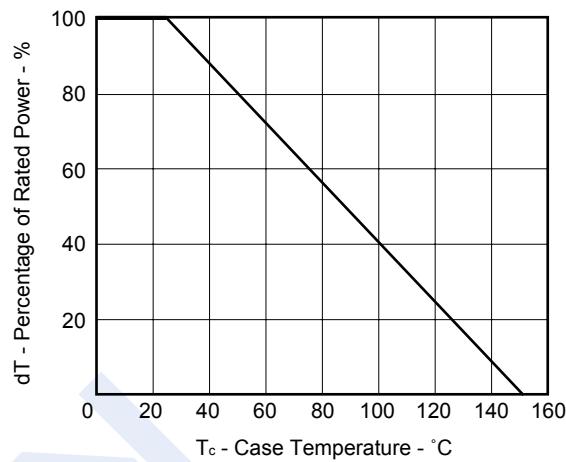
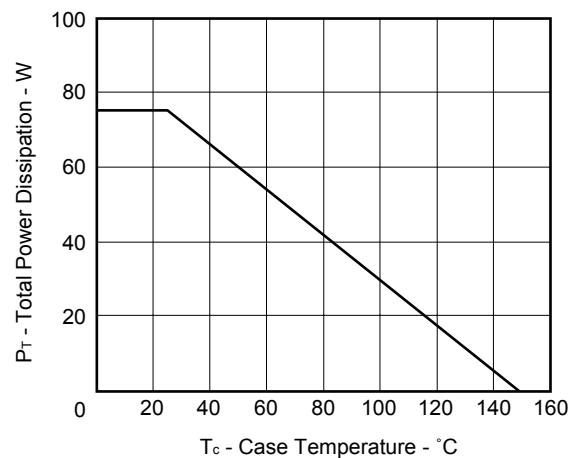
## N-Channel MOSFET

## 2SK3305-ZJ

■ Electrical Characteristics  $T_a = 25^\circ\text{C}$ 

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{DSS}$	$I_D=250 \mu\text{A}, V_{GS}=0\text{V}$	500			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=500\text{V}, V_{GS}=0\text{V}$			100	$\mu\text{A}$
Gate-Body Leakage Current	$I_{GSS}$	$V_{DS}=0\text{V}, V_{GS}=\pm 30\text{V}$			$\pm 100$	nA
Gate to Source Cut-off Voltage	$V_{GS(\text{off})}$	$V_{DS}=10\text{V}, I_D=1\text{mA}$	2.5		3.5	V
Static Drain-Source On-Resistance	$R_{DS(\text{ON})}$	$V_{GS}=10\text{V}, I_D=2.5\text{A}$			1.5	$\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS}=10\text{V}, I_D=2.5\text{A}$	1	3		S
Input Capacitance	$C_{iss}$	$V_{GS}=0\text{V}, V_{DS}=10\text{V}, f=1\text{MHz}$		700		pF
Output Capacitance	$C_{oss}$			115		
Reverse Transfer Capacitance	$C_{rss}$			6		
Total Gate Charge	$Q_g$	$V_{GS}=10\text{V}, V_{DS}=400\text{V}, I_D=5\text{A}$		13		nC
Gate Source Charge	$Q_{gs}$			4		
Gate Drain Charge	$Q_{gd}$			4.5		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 150\text{ V}, I_D = 2.5\text{ A}, V_{GS(on)} = 10\text{ V}, R_G = 10 \Omega, R_L = 60 \Omega$		16		ns
Turn-On Rise Time	$t_r$			3		
Turn-Off Delay Time	$t_{d(off)}$			33		
Turn-Off Fall Time	$t_f$			5.5		
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 5\text{ A}, V_{GS}=0, dI/dt = 50\text{ A}/\mu\text{s}$		600		uC
Body Diode Reverse Recovery Charge	$Q_{rr}$			3.3		
Diode Forward Voltage	$V_{SD}$	$I_F=5\text{A}, V_{GS}=0\text{V}$		0.9		V

## ■ Typical Characteristics

Figure1. DERATING FACTOR OF FORWARD BIAS  
SAFE OPERATING AREAFigure2. TOTAL POWER DISSIPATION vs.  
CASE TEMPERATURE

**N-Channel MOSFET****2SK3305-ZJ****■ Typical Characteristics**

Figure3. FORWARD BIAS SAFE OPERATING AREA

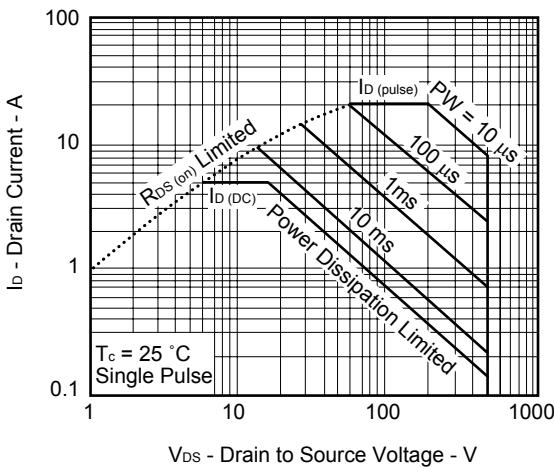


Figure4. DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE

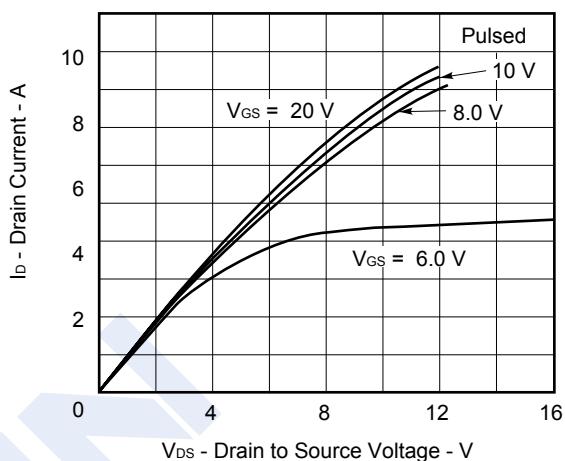


Figure5. DRAIN CURRENT vs. GATE TO SOURCE VOLTAGE

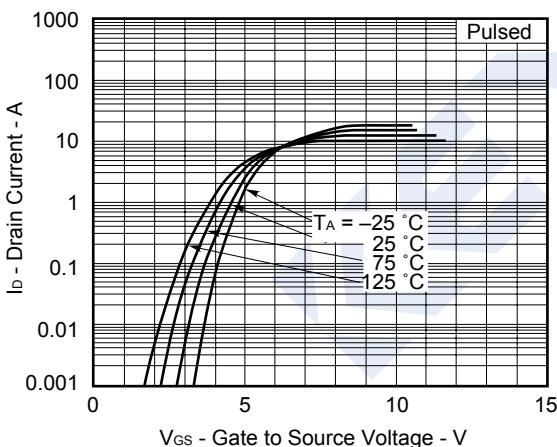
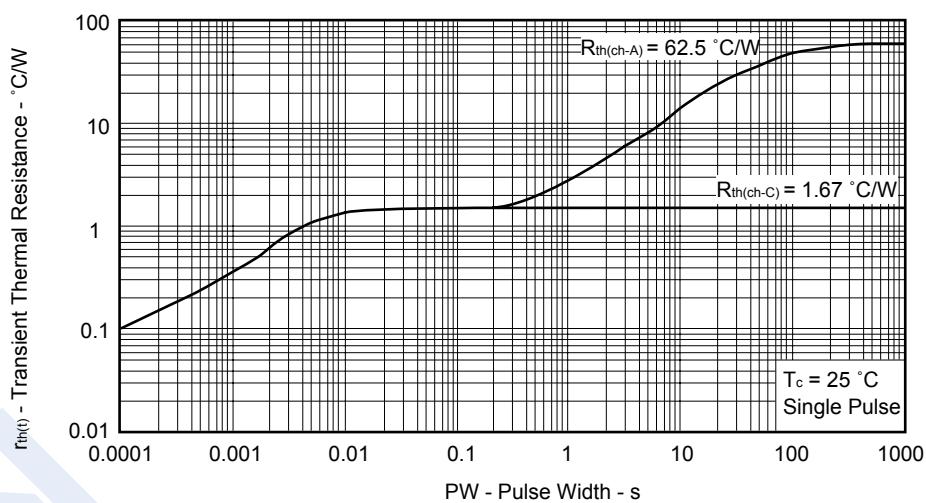


Figure6. TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



## N-Channel MOSFET

### 2SK3305-ZJ

#### ■ Typical Characteristics

Figure7. FORWARD TRANSFER ADMITTANCE vs.  
DRAIN CURRENT

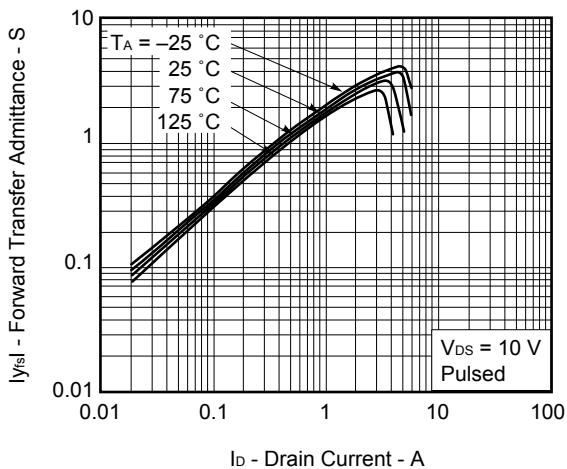


Figure9. DRAIN TO SOURCE ON-STATE  
RESISTANCE vs. DRAIN CURRENT

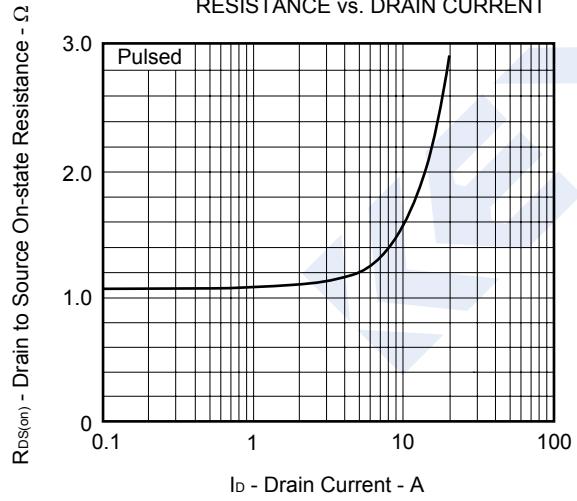


Figure11. DRAIN TO SOURCE ON-STATE RESISTANCE vs.  
CHANNEL TEMPERATURE

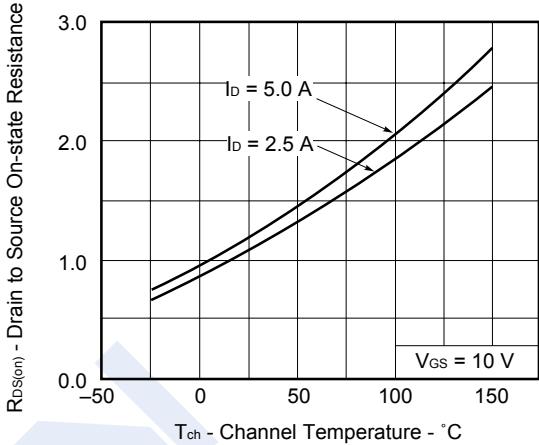


Figure8. DRAIN TO SOURCE ON-STATE RESISTANCE vs.  
GATE TO SOURCE VOLTAGE

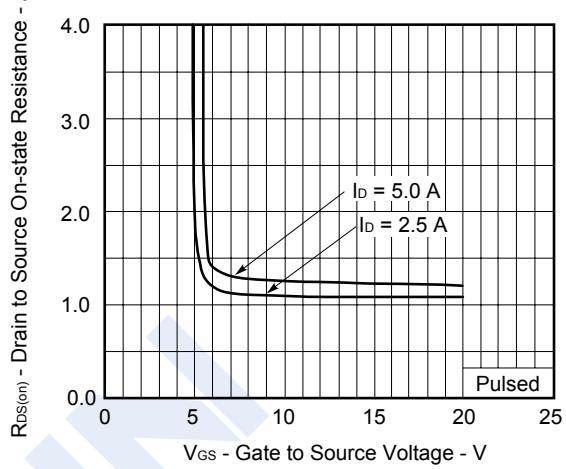


Figure10. GATE TO SOURCE CUT-OFF VOLTAGE vs.  
CHANNEL TEMPERATURE

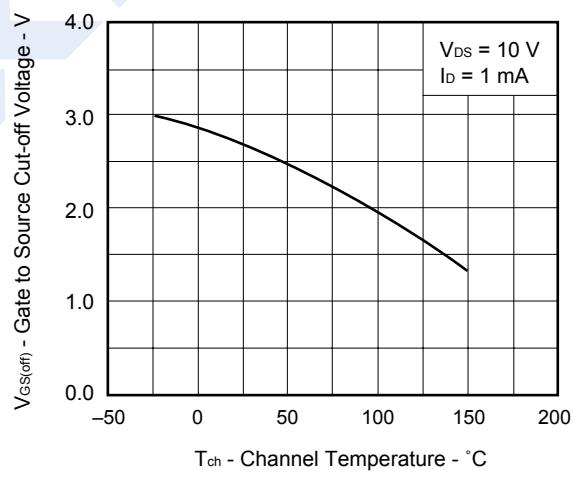
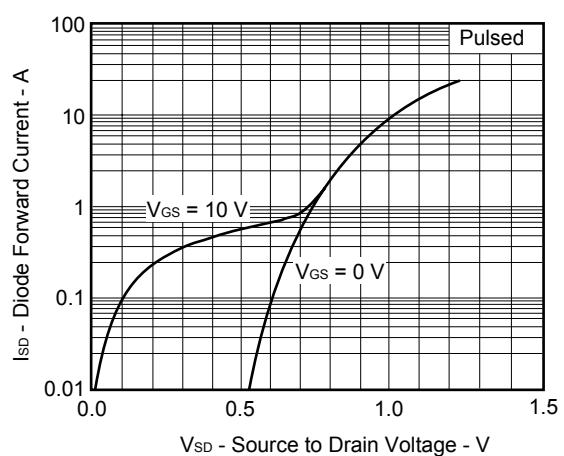


Figure12. SOURCE TO DRAIN DIODE  
FORWARD VOLTAGE



## N-Channel MOSFET

## 2SK3305-ZJ

## ■ Typical Characteristics

Figure13. CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE

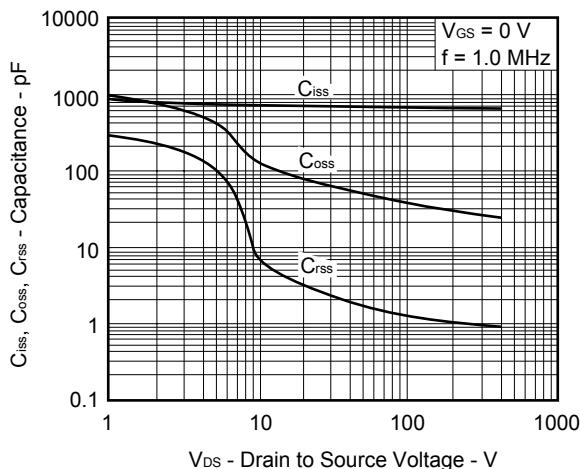


Figure15. REVERSE RECOVERY TIME vs. DRAIN CURRENT

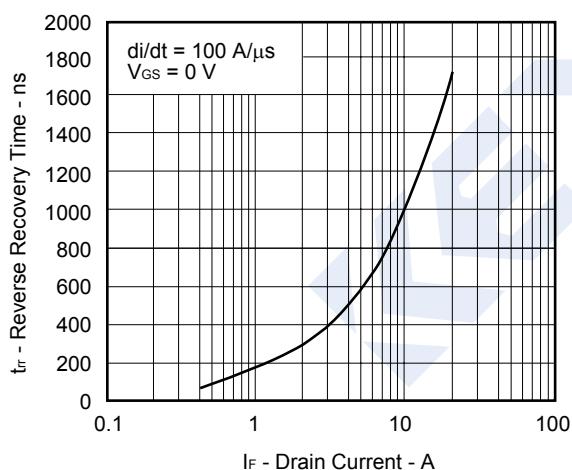


Figure17. SINGLE AVALANCHE ENERGY vs STARTING CHANNEL TEMPERATURE

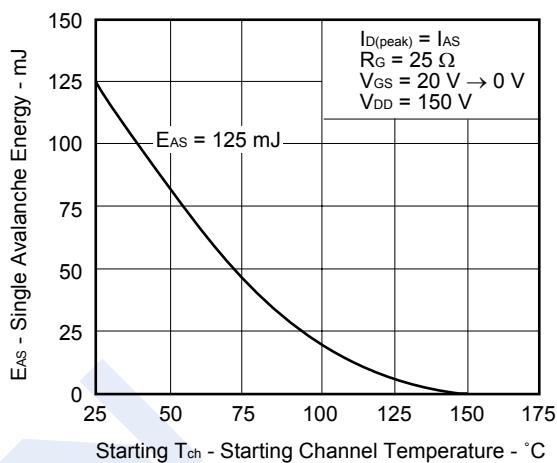


Figure14. SWITCHING CHARACTERISTICS

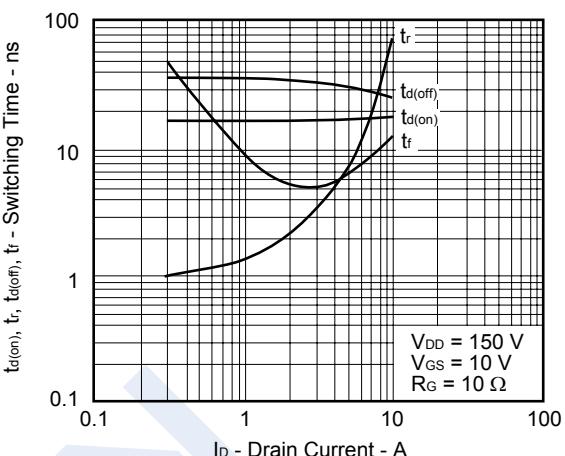


Figure16. DYNAMIC INPUT/OUTPUT CHARACTERISTICS

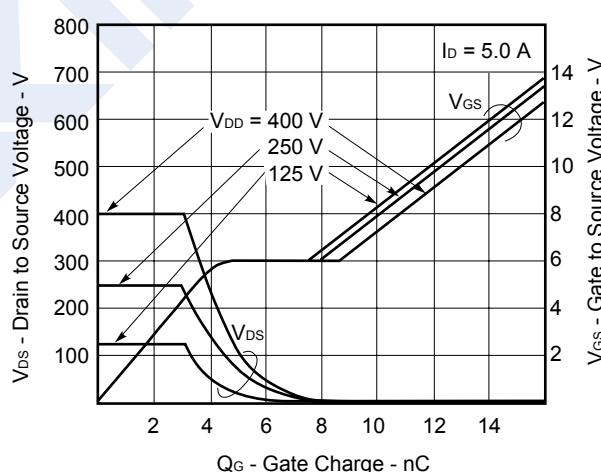


Figure18. SINGLE AVALANCHE CURRENT vs INDUCTIVE LOAD

