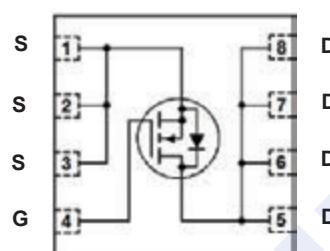
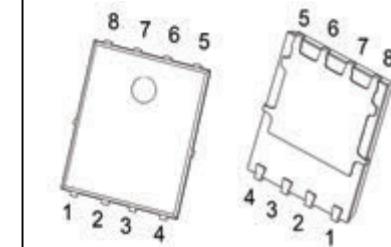


N-Channel MOSFET**2KK5085DFN****■ Features**

- V_{DS} (V) = 100 V
- I_D = 60 A
- $R_{DS(ON)}$ (at V_{GS} = 10 V) < 8.2 mΩ

**PDFN5x6-8****■ Absolute Maximum Ratings ($T_c = 25^\circ\text{C}$ unless otherwise noted)**

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current $T_c = 100^\circ\text{C}$	I_D	60	A
		42.5	
Pulsed Drain Current (Note 1)	I_{DM}	240	
Power Dissipation	P_D	105	W
Derating factor		0.84	W/°C
Single Pulse Avalanche Energy (Note 2)	E_{AS}	250	mJ
Thermal Resistance Junction-to-Case (Note 3)	$R_{\theta JC}$	1.2	°C/W
Junction Temperature	T_J	150	°C
Storage Temperature Range	T_{stg}	-55 to 150	

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. EAS condition : $T_j=25^\circ\text{C}$, $V_{DD}=50\text{V}$, $V_G=10\text{V}$, $L=0.5\text{mH}$, $R_g=25\Omega$
3. Surface Mounted on FR4 Board, $t \leqslant 10$ sec.

N-Channel MOSFET

2KK5085DFN

■ Electrical Characteristics ($T_C = 25^\circ\text{C}$ unless otherwise specified)

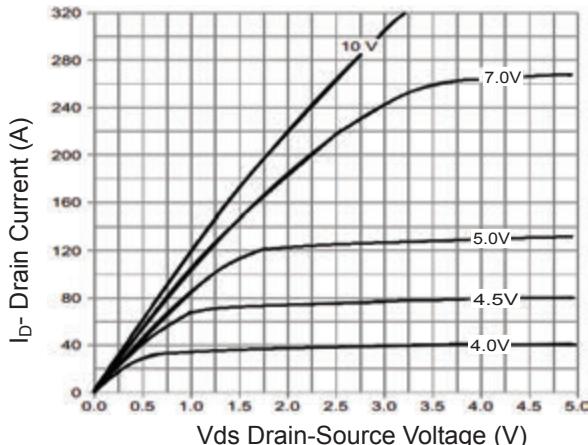
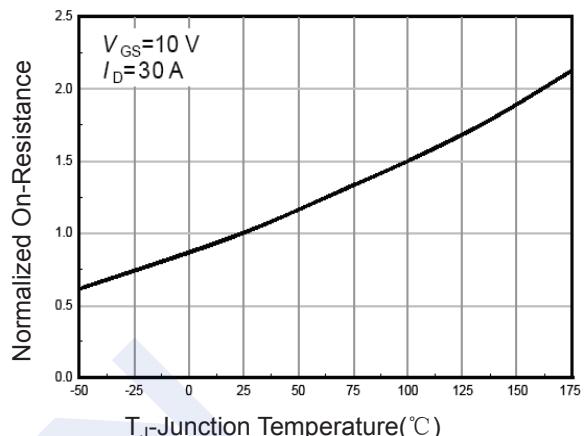
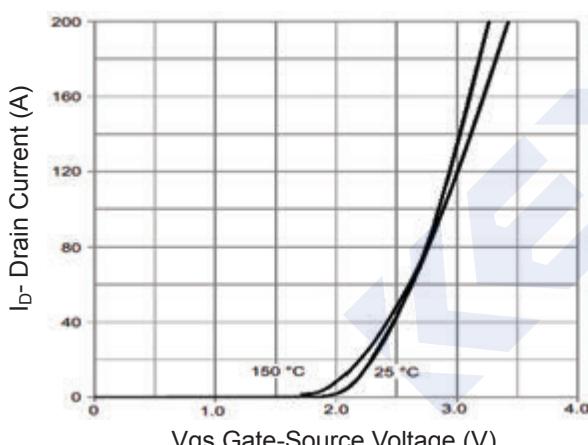
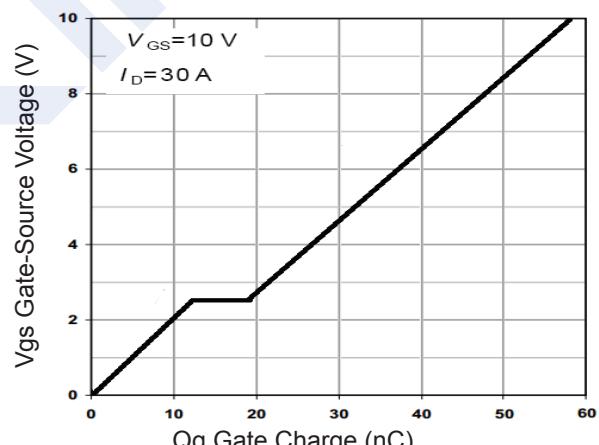
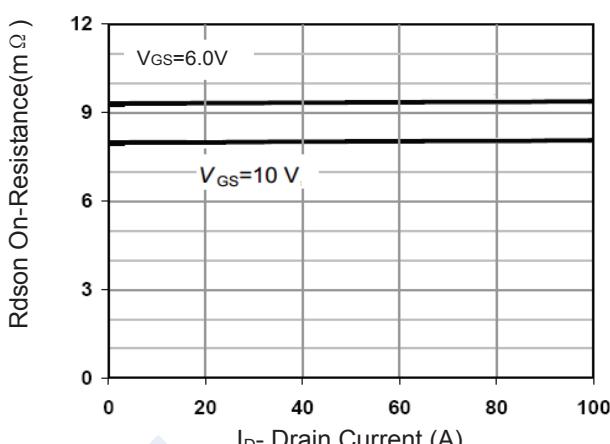
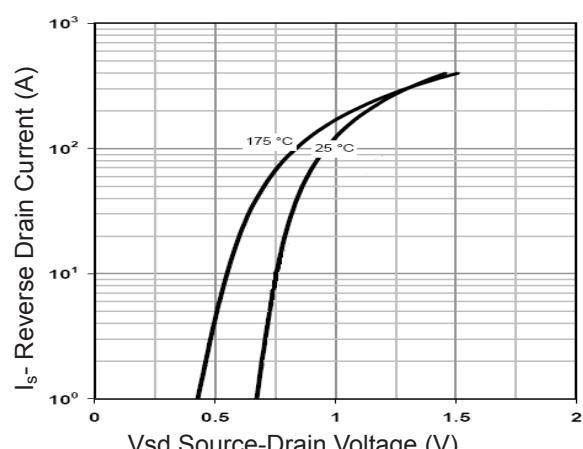
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{I}_D = 250 \mu\text{A}, \text{V}_{\text{GS}} = 0\text{V}$	100			V
Zero Gate Voltage Drain Current	$\text{I}_{\text{DS}}^{\text{SS}}$	$\text{V}_{\text{DS}} = 100\text{V}, \text{V}_{\text{GS}} = 0\text{V}$			1	μA
Gate to Source Leakage Current	I_{GSS}	$\text{V}_{\text{DS}} = 0\text{V}, \text{V}_{\text{GS}} = \pm 20\text{V}$			± 100	nA
On Characteristics (Note 1)						
Gate to Source Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}, \text{I}_D = 250\mu\text{A}$	1.0	1.7	2.2	V
Static Drain-Source On-Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}} = 10\text{V}, \text{I}_D = 30\text{A}$			8.2	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$\text{V}_{\text{DS}} = 10\text{V}, \text{I}_D = 30\text{A}$	40			S
Dynamic Characteristics (Note 1)						
Input Capacitance	C_{iss}	$\text{V}_{\text{GS}} = 0\text{V}, \text{V}_{\text{DS}} = 50\text{V}, \text{f} = 1\text{MHz}$		4200		pF
Output Capacitance	C_{oss}			354		
Reverse Transfer Capacitance	C_{rss}			23		
Switching Characteristics (Note 1)						
Total Gate Charge	Q_g	$\text{V}_{\text{GS}} = 10\text{V}, \text{V}_{\text{DS}} = 50\text{V}, \text{I}_D = 30\text{A}$		58		nC
Gate Source Charge	Q_{gs}			12		
Gate Drain Charge	Q_{gd}			7.8		
Turn-On Delay Time	$\text{t}_{\text{d(on)}}$	$\text{V}_{\text{GS}} = 10\text{V}, \text{V}_{\text{DD}} = 50\text{V}, \text{I}_D = 30\text{A}, \text{R}_G = 4.7\Omega$		14		ns
Turn-On Rise Time	t_r			9		
Turn-Off Delay Time	$\text{t}_{\text{d(off)}}$			39		
Turn-Off Fall Time	t_f			5		
Drain-Source Diode Characteristics						
Body Diode Reverse Recovery Time	t_{rr}	$\text{I}_F = \text{I}_S, \frac{d\text{I}}{dt} = 100\text{A}/\mu\text{s}, \text{T}_J = 25^\circ\text{C}$		58		ns
Body Diode Reverse Recovery Charge	Q_{rr}			110		nC
Maximum Body-Diode Continuous Current	I_S				60	A
Diode Forward Voltage	V_{SD}	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_S = 60\text{A}$			1.2	V

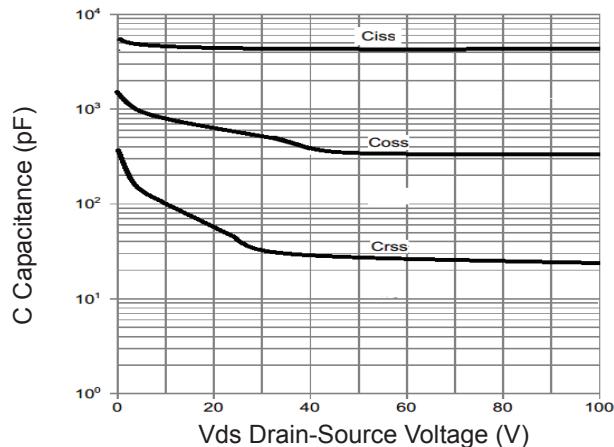
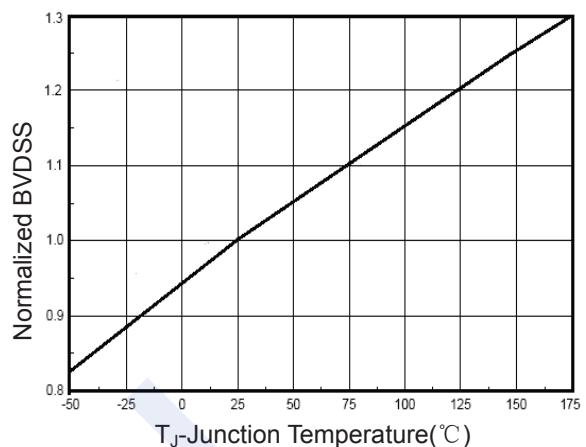
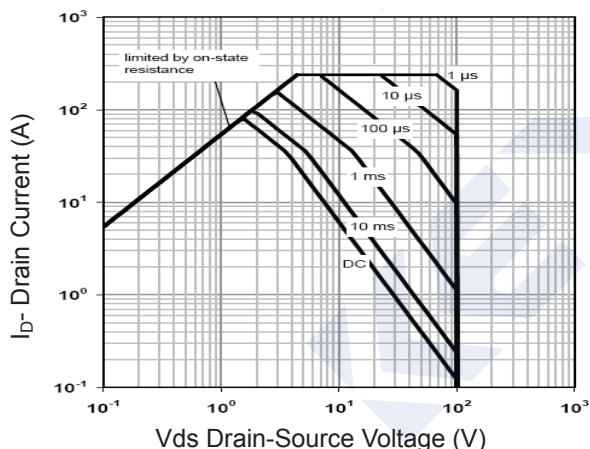
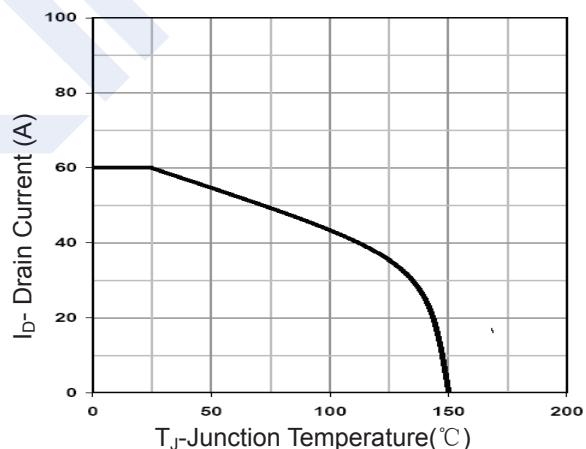
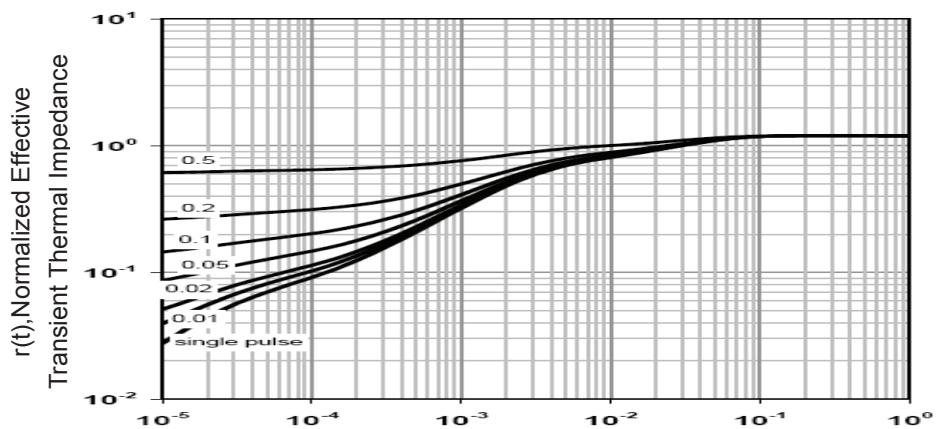
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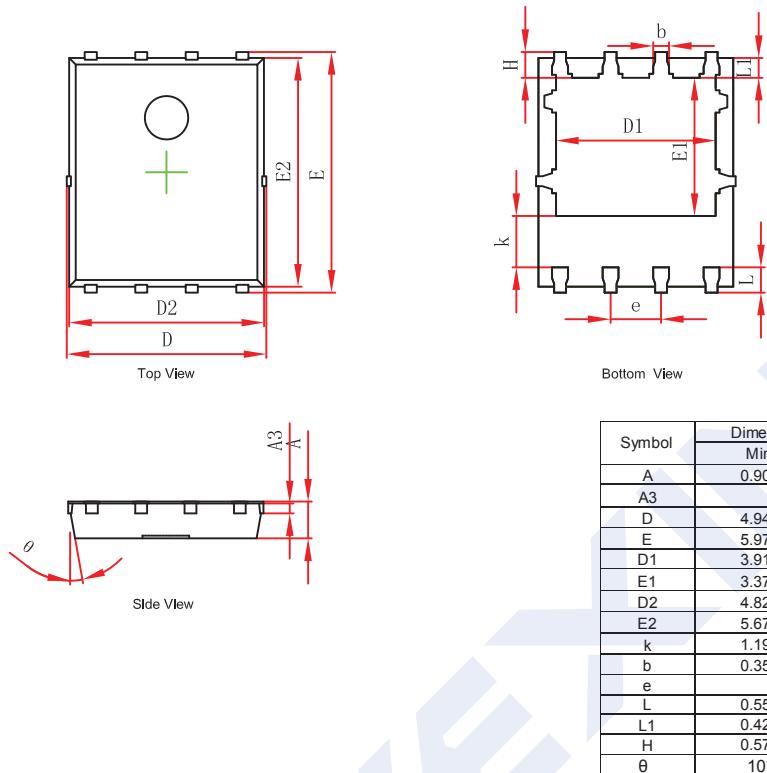
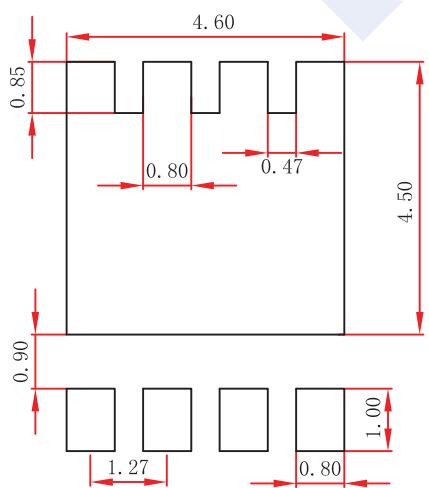
1. Pulse Test: Pulse Width $\leqslant 300\mu\text{s}$, Duty Cycle $\leqslant 2\%$.

■ Marking

Marking	K5085 KC***
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N-Channel MOSFET**2KK5085DFN****■ Typical Electrical and Thermal Characteristics****Figure 1 Output Characteristics****Figure 4 Rdson-JunctionTemperature****Figure 2 Transfer Characteristics****Figure 5 Gate Charge****Figure 3 Rdson- Drain Current****Figure 6 Source- Drain Diode Forward**

N-Channel MOSFET**2KK5085DFN****Figure 7 Capacitance vs Vds****Figure 9 BV_{DSS} vs Junction Temperature****Figure 8 Safe Operation Area****Figure 10 Current De-rating****Figure 11 Normalized Maximum Transient Thermal Impedance**

N-Channel MOSFET**2KK5085DFN****■ PDFN5x6-8 Package Outline Dimensions****■ DFN5x6-8(PDFNWB5x6-8L) Suggested Pad Layout****Note:**

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.