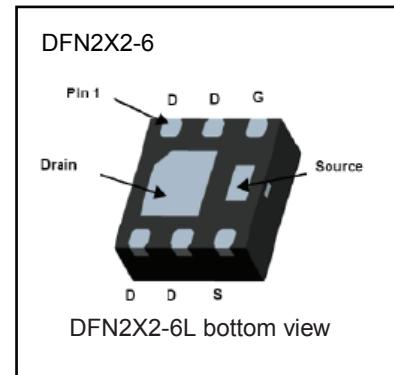
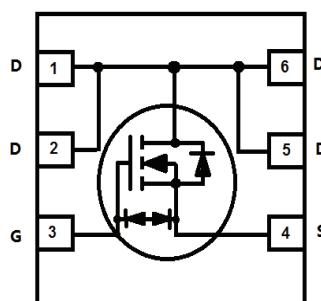


N-Channel MOSFET**2KK5018DFN****■ Features**

- $V_{DS} = 20 \text{ V}$
- $I_D = 12 \text{ A}$
- Low Gate Threshold Voltage
- Fast Switching Speed
- ESD Protected Gate

**■ Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)**

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 10	
Continuous Drain Current	I_D	12	A
Pulsed Drain Current	I_{DM}	40	
Power Dissipation	P_D	1.4	W
		0.9	
Thermal Resistance, Junction- to-Ambient (Note 1)	R_{JA}	90	$^\circ\text{C}/\text{W}$
Operating Junction Temperature	T_J	-55 to 150	$^\circ\text{C}$

1. The device mounted on 1in² FR4 board with 2 oz copper

N-Channel MOSFET**2KK5018DFN****■ Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)**

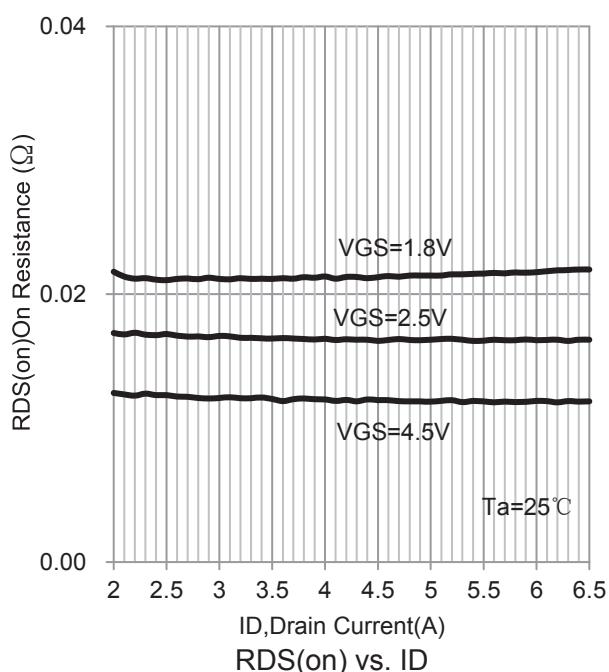
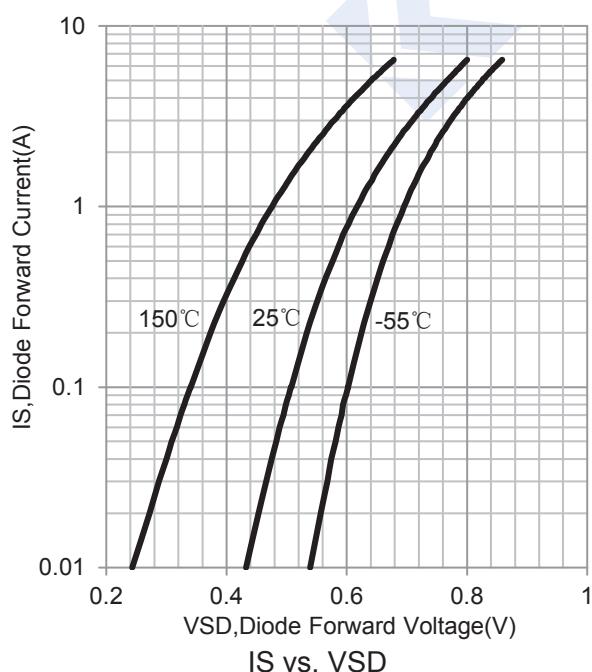
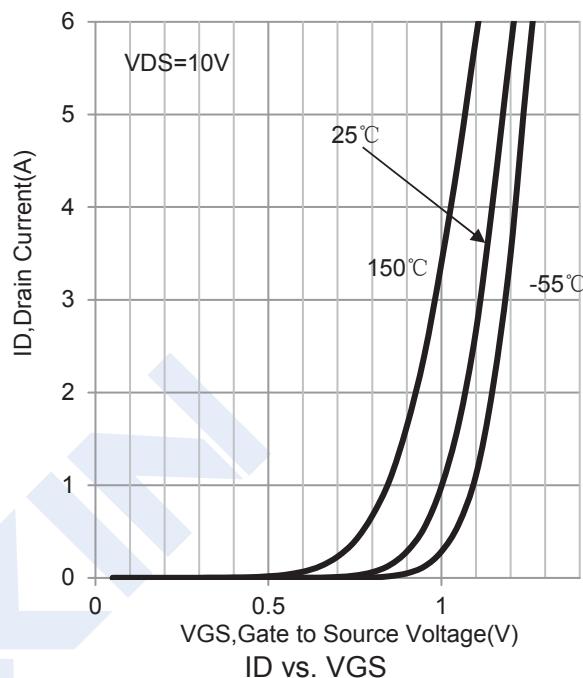
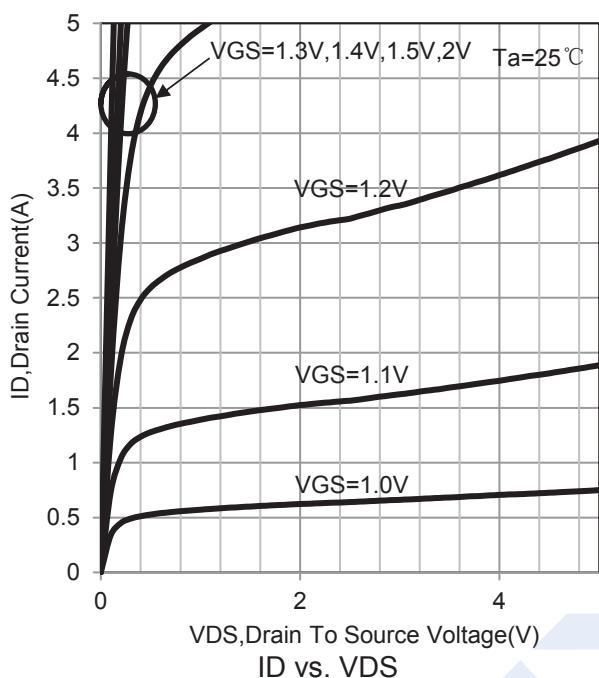
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{I}_D = 250 \mu\text{A}, \text{V}_{\text{GS}} = 0\text{V}$	20			V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}} = 20 \text{ V}, \text{V}_{\text{GS}} = 0 \text{ V}$			1	μA
Gate to Source Leakage Current	I_{GSS}	$\text{V}_{\text{DS}} = 0 \text{ V}, \text{V}_{\text{GS}} = \pm 10 \text{ V}$			± 10	μA
Gate to Source Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}, \text{I}_D = 250 \mu\text{A}$	0.4		1	V
Static Drain-Source On-Resistance (Note 2)	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}} = 4.5 \text{ V}, \text{I}_D = 5 \text{ A}$			15	$\text{m}\Omega$
		$\text{V}_{\text{GS}} = 2.5 \text{ V}, \text{I}_D = 5 \text{ A}$			18	
		$\text{V}_{\text{GS}} = 1.8 \text{ V}, \text{I}_D = 5 \text{ A}$			30	
Input Capacitance	C_{iss}	$\text{V}_{\text{GS}} = 0 \text{ V}, \text{V}_{\text{DS}} = 10 \text{ V}, \text{f} = 1 \text{ MHz}$		150		pF
Output Capacitance	C_{oss}			95		
Reverse Transfer Capacitance	C_{rss}			25		
Total Gate Charge	Q_g	$\text{V}_{\text{GS}} = 4.5 \text{ V}, \text{V}_{\text{DS}} = 10 \text{ V}, \text{I}_D = 6.5 \text{ A}$		10		nC
Gate Source Charge	Q_{gs}			0.9		
Gate Drain Charge	Q_{gd}			3		
Turn-On Delay Time	$\text{t}_{\text{d(on)}}$	$\text{V}_{\text{GS}} = 5 \text{ V}, \text{RL} = 1.5 \Omega, \text{V}_{\text{DS}} = 10 \text{ V}, \text{R}_{\text{GEN}} = 3 \Omega$		250		ns
Turn-On Rise Time	t_r			420		
Turn-Off Delay Time	$\text{t}_{\text{d(off)}}$			3950		
Turn-Off Fall Time	t_f			3700		
Diode Forward Voltage	V_{SD}	$\text{V}_{\text{GS}} = 0 \text{ V}, \text{I}_s = 5 \text{ A}$			1	V

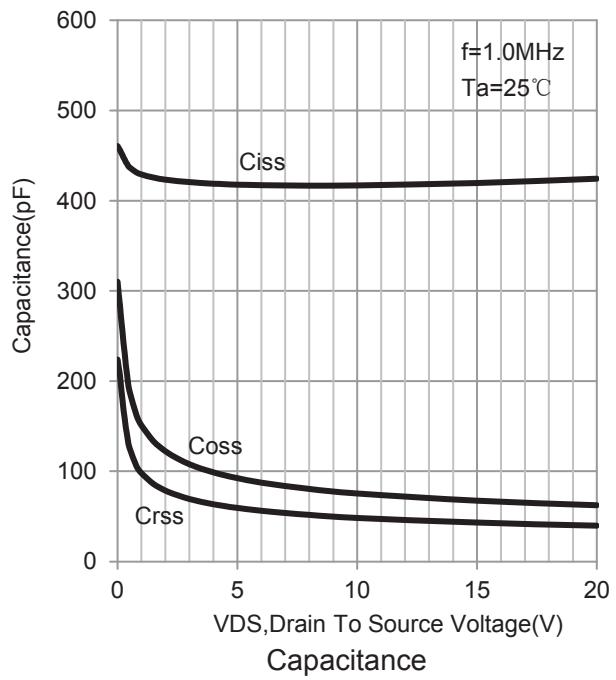
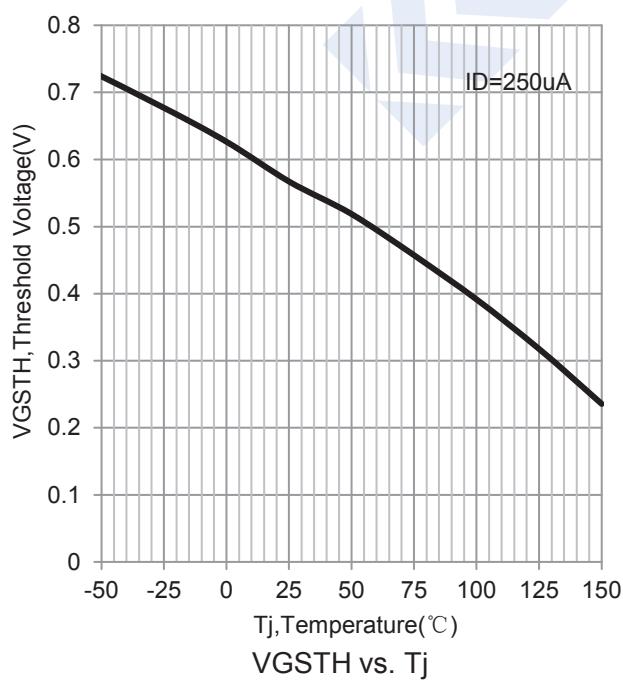
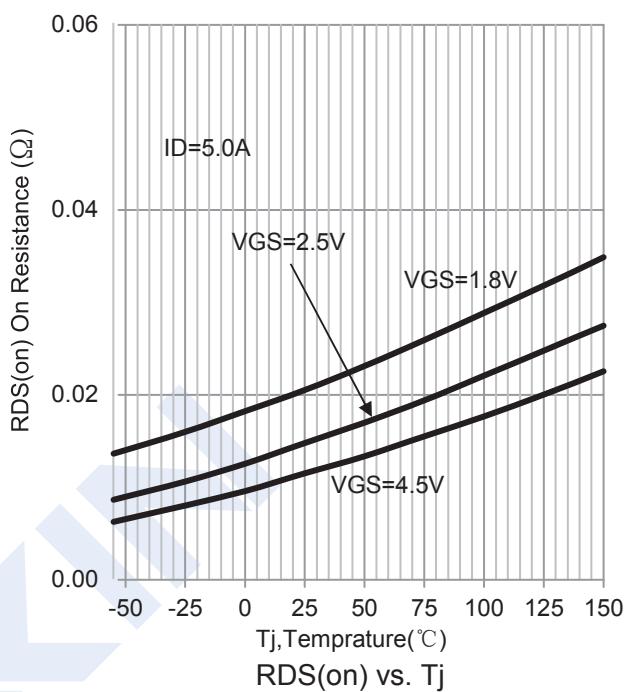
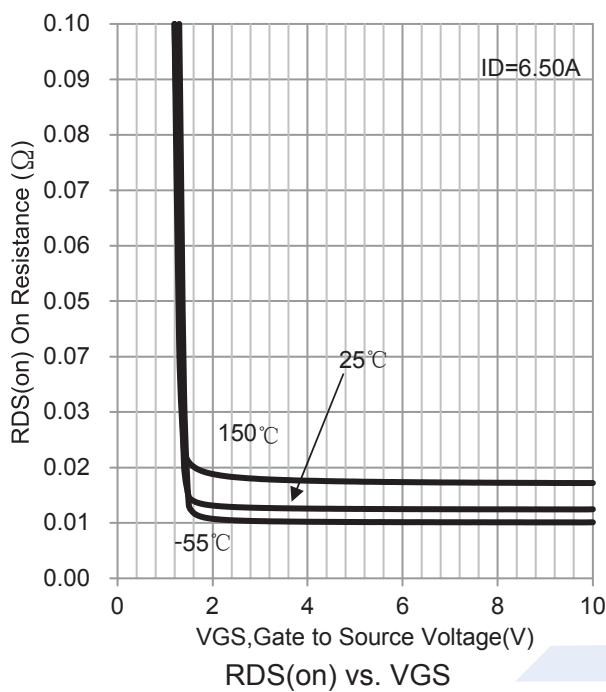
2. Pulse test; pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$ **■ Ordering Information**

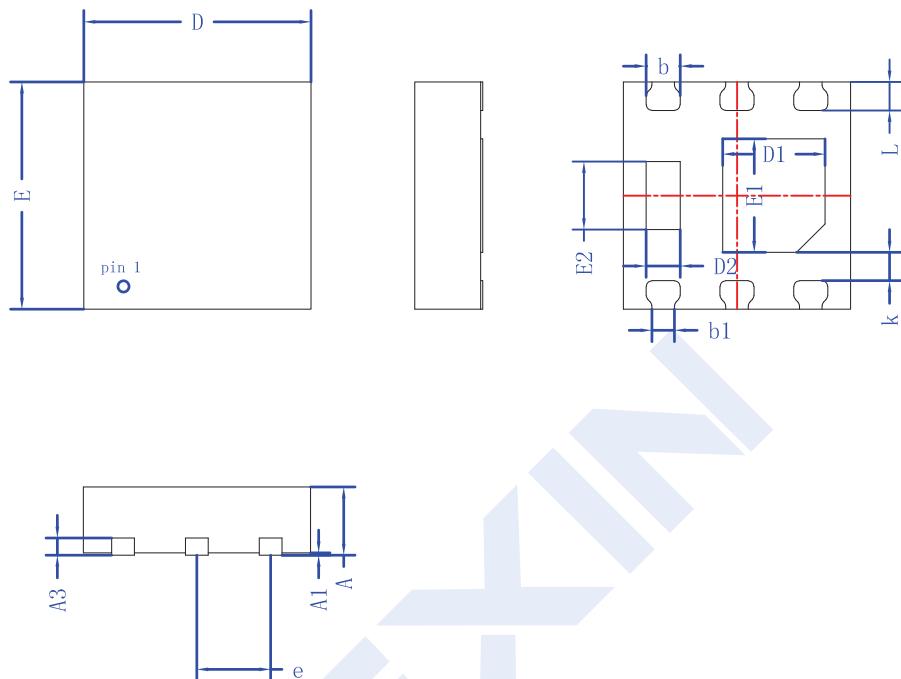
Device	Marking	Shipping
2KK5018DFN	2408E	4000/Tape&Reel

N-Channel MOSFET**2KK5018DFN**

■ Typical Characteristics



N-Channel MOSFET**2KK5018DFN**

N-Channel MOSFET**2KK5018DFN****■ DFN2X2-6 Package Outline Dimensions**

Symbol	Dimensions In Millimeters			Dimensions In Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.50	0.55	0.65	0.022	0.024	0.026
A1	0.00	0.02	0.05	0.000	0.001	0.002
A3	0.152 REF.			0.006REF.		
D	1.90	2.00	2.10	0.075	0.079	0.083
D1	0.80	0.90	1.00	0.031	0.035	0.039
D2	0.20	0.30	0.40	0.008	0.012	0.016
E	1.90	2.00	2.10	0.075	0.079	0.083
E1	0.90	1.00	1.10	0.035	0.039	0.043
E2	0.50	0.60	0.70	0.020	0.024	0.028
b	0.25	0.30	0.35	0.010	0.012	0.014
b1	0.15	0.20	0.25	0.006	0.008	0.010
e	0.65TYP.			0.026TYP.		
k	0.20MIN.			0.006MIN.		
L	0.20	0.25	0.30	0.008	0.010	0.012